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**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = GARLETT

First Name = JON

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">10201868</a>	<a href="#">7085970</a>	150	07/23/2002	FAST DETECTION OF INCORRECT SAMPLING IN AN OVERSAMPLING CLOCK AND DATA RECOVERY SYSTEM	GARLETT, JON D.
<a href="#">10644372</a>	<a href="#">7038319</a>	150	08/20/2003	APPARATUS AND METHOD TO REDUCE SIGNAL CROSS-TALK	GARLETT, JON D.
<a href="#">10064387</a>	Not Issued	71	07/09/2002	Data communication system with self-test feature	GARLETT, JON DAVID

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**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = MOY

First Name = VICTOR

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10064387	Not Issued	71	07/09/2002	Data communication system with self-test feature	MOY, VICTOR
10139931	6661267	150	05/06/2002	COARSE CALIBRATION CIRCUIT USING VARIABLE STEP SIZES TO REDUCE JITTER AND A DYNAMIC COURSE CALIBRATION (DCC) CIRCUIT FOR A 2 GHZ VCO	MOY, VICTOR
11160218	Not Issued	25	06/14/2005	MULTI-CHANNEL SYNCHRONIZATION ARCHITECTURE	MOY, VICTOR

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## Inventor Name Search Result

Your Search was:

Last Name = SORNA

First Name = MICHAEL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#"><u>09757107</u></a>	<a href="#"><u>6466100</u></a>	150	01/08/2001	LINEAR VOLTAGE CONTROLLED OSCILLATOR TRANSCONDUCTOR WITH GAIN COMPENSATION	SORNA, MICHAEL A.
<a href="#"><u>09761526</u></a>	<a href="#"><u>6528777</u></a>	150	01/16/2001	OPTICAL POWER METER DERIVED FROM COMMON-MODE VOLTAGE OF OPTICAL TRANSIMPEDANCE AMPLIFIER	SORNA, MICHAEL A.
<a href="#"><u>09887792</u></a>	<a href="#"><u>6785832</u></a>	150	06/22/2001	PROCESS INDEPENDENT SOURCE SYNCHRONOUS DATA CAPTURE APPARATUS AND METHOD	SORNA, MICHAEL A.
<a href="#"><u>10064387</u></a>	Not Issued	71	07/09/2002	Data communication system with self-test feature	SORNA, MICHAEL A.
<a href="#"><u>10139931</u></a>	<a href="#"><u>6661267</u></a>	150	05/06/2002	COARSE CALIBRATION CIRCUIT USING VARIABLE STEP SIZES TO REDUCE JITTER AND A DYNAMIC COURSE CALIBRATION (DCC) CIRCUIT FOR A 2 GHZ VCO	SORNA, MICHAEL A.
<a href="#"><u>10160541</u></a>	Not Issued	93	05/31/2002	ON-CHIP SYSTEM AND METHOD FOR MEASURING JITTER TOLERANCE OF A CLOCK AND DATA RECOVERY CIRCUIT	SORNA, MICHAEL A.
<a href="#"><u>10249545</u></a>	<a href="#"><u>6891357</u></a>	150	04/17/2003	REFERENCE CURRENT GENERATION SYSTEM AND METHOD	SORNA, MICHAEL A.
<a href="#"><u>10249546</u></a>	<a href="#"><u>6980824</u></a>	150	04/17/2003	METHOD AND SYSTEM FOR OPTIMIZING TRANSMISSION AND RECEPTION POWER LEVELS IN A COMMUNICATION SYSTEM	SORNA, MICHAEL A.

<a href="#"><u>10249795</u></a>	<a href="#"><u>6680681</u></a>	150	05/08/2003	HIGH SPEED FIR TRANSMITTER	SORNA, MICHAEL A.
<a href="#"><u>10250043</u></a>	<a href="#"><u>6937054</u></a>	150	05/30/2003	PROGRAMMABLE PEAKING RECEIVER AND METHOD	SORNA, MICHAEL A.
<a href="#"><u>10604025</u></a>	Not Issued	30	06/23/2003	DATA TRANSCEIVER AND METHOD FOR EQUALIZING THE DATA EYE OF A DIFFERENTIAL INPUT DATA SIGNAL	SORNA, MICHAEL A.
<a href="#"><u>10604419</u></a>	<a href="#"><u>7113749</u></a>	150	07/18/2003	SYSTEM AND METHOD FOR MEASURING A HIGH SPEED SIGNAL	SORNA, MICHAEL A.
<a href="#"><u>10707123</u></a>	<a href="#"><u>6956417</u></a>	150	11/21/2003	LEAKAGE COMPENSATION CIRCUIT	SORNA, MICHAEL A.
<a href="#"><u>10707171</u></a>	<a href="#"><u>6963240</u></a>	150	11/25/2003	DAMPING OF LC RINGING IN IC (INTEGRATED CIRCUIT) POWER DISTRIBUTION SYSTEMS	SORNA, MICHAEL A.
<a href="#"><u>10708233</u></a>	<a href="#"><u>6949981</u></a>	150	02/18/2004	DYNAMIC THRESHOLD FOR VCO CALIBRATION	SORNA, MICHAEL A.
<a href="#"><u>10710064</u></a>	Not Issued	41	06/16/2004	AUTOMATIC ADAPTIVE EQUALIZATION METHOD AND SYSTEM FOR HIGH-SPEED SERIAL TRANSMISSION LINK	SORNA, MICHAEL A.
<a href="#"><u>10710745</u></a>	<a href="#"><u>7053712</u></a>	150	07/30/2004	METHOD AND APPARATUS FOR CONTROLLING COMMON-MODE OUTPUT VOLTAGE IN FULLY DIFFERENTIAL AMPLIFIERS	SORNA, MICHAEL A.
<a href="#"><u>10711713</u></a>	Not Issued	30	09/30/2004	High Speed Multi-Mode Receiver	SORNA, MICHAEL A.
<a href="#"><u>10905704</u></a>	<a href="#"><u>7102392</u></a>	150	01/18/2005	IMPROVED SIGNAL DETECTOR FOR HIGH-SPEED SERDES	SORNA, MICHAEL A.
<a href="#"><u>10905705</u></a>	Not Issued	30	01/18/2005	FRONT END INTERFACE FOR DATA RECEIVER	SORNA, MICHAEL A.
<a href="#"><u>10908959</u></a>	Not Issued	20	06/02/2005	APPARATUS AND METHOD FOR REDUCED LOADING OF SIGNAL TRANSMISSION ELEMENTS	SORNA, MICHAEL A.
<a href="#"><u>10994742</u></a>	Not Issued	30	11/22/2004	Timing bias compensation for a data receiver with decision-feedback equalizer	SORNA, MICHAEL A.
<a href="#"><u>11103314</u></a>	Not	93	04/11/2005	REFERENCE CURRENT	SORNA, MICHAEL

	Issued			GENERATION SYSTEM	A.
<a href="#"><u>11306985</u></a>	Not Issued	20	01/18/2006	ON-CHIP ELECTROMIGRATION MONITORING SYSTEM	SORNA, MICHAEL A.
<a href="#"><u>11383821</u></a>	Not Issued	20	05/17/2006	Signal Detector with Calibration Circuit Arrangement	SORNA, MICHAEL A.
<a href="#"><u>11467349</u></a>	Not Issued	25	08/25/2006	CML TO CMOS SIGNAL CONVERTER	SORNA, MICHAEL A.
<a href="#"><u>07620973</u></a>	5132613	150	11/30/1990	LOW INDUCTANCE SIDE MOUNT DECOUPLING TEST STRUCTURE	SORNA, MICHAEL A.
<a href="#"><u>07690404</u></a>	5144228	150	04/23/1991	PROBE INTERFACE ASSEMBLY	SORNA, MICHAEL A.
<a href="#"><u>08534900</u></a>	5661395	250	09/28/1995	ACTIVE, LOW VSD, FIELD EFFECT TRANSISTOR CURRENT SOURCE	SORNA, MICHAEL A.
<a href="#"><u>08884117</u></a>	5912928	150	06/27/1997	HIGH SPEED SERIAL DATA TRANSMISSION ENCODER	SORNA, MICHAEL A.
<a href="#"><u>09017719</u></a>	5825169	150	02/04/1998	DYNAMICALLY BIASED CURRENT GAIN VOLTAGE REGULATOR WITH LOW QUIESCENT POWER CONSUMPTION	SORNA, MICHAEL A.
<a href="#"><u>10915790</u></a>	Not Issued	30	08/11/2004	Methods and arrangements for link power reduction	SORNA, MICHAEL ANTHONY
<a href="#"><u>08740811</u></a>	5805088	150	11/01/1996	HIGH SPEED ASYNCHRONOUS SERIAL TO PARALLEL DATA CONVERTER	SORNA, MICHAEL ANTHONY

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